

Specifications

Circuit Board Dimension	65mm x 50mm
Power Requirements	USB Power : DC +5V (Power provided by USB cable, No Power supply required) Target FPGA Power : 1.8V, 3.3V Target DSP Power : 1.8V, 3.3V
Operating Temperature	-40 to 85°C
Storage Temperature	-40 to 85°C
Programming	Programming target FPGA using Xilinx Vivado Programming target DSP using TI Code Composer Studio

Case Option

DT-JTAGMUL-0x

- (1) -01 : Plastic, Black
- (2) -02 : Metal, Black

Product Package

- (1) DT-JTAGMUL-0x (FPGA / DSP JTAG-Emulator)
- (2) Power Cable (USB A to B, 2M)
- (3) FPGA-JTAG Cable (0.16M)
- (4) DSP-JTAG Cable (0.16M)



DT-JTAGMUL0x



Power cable

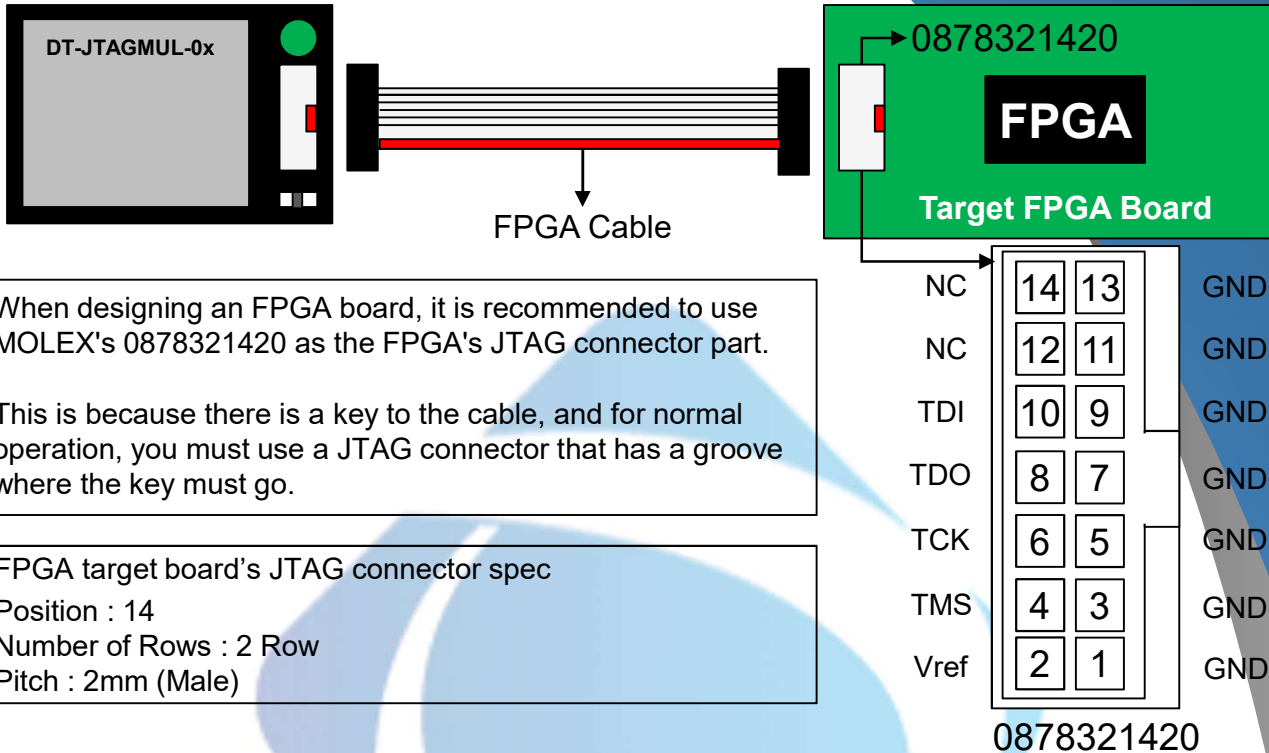


FPGA-JTAG cable

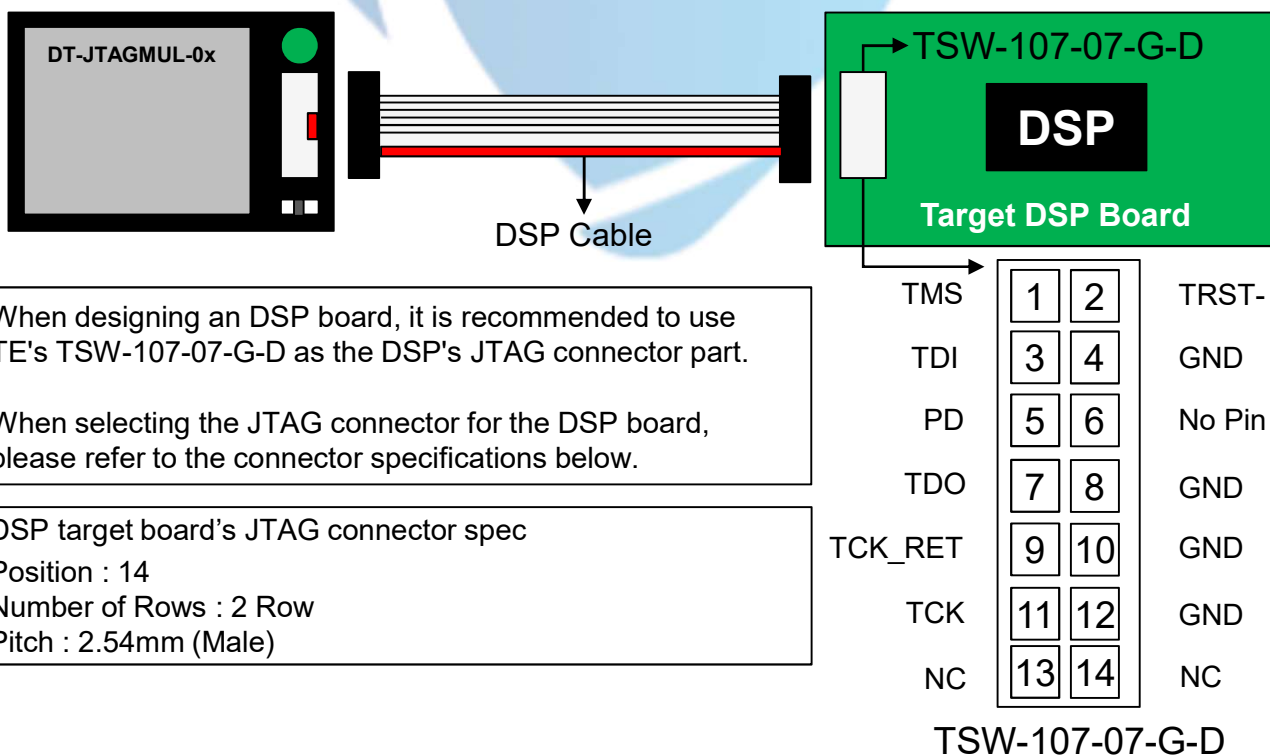


Pin Assign

1. FPGA board pin assign



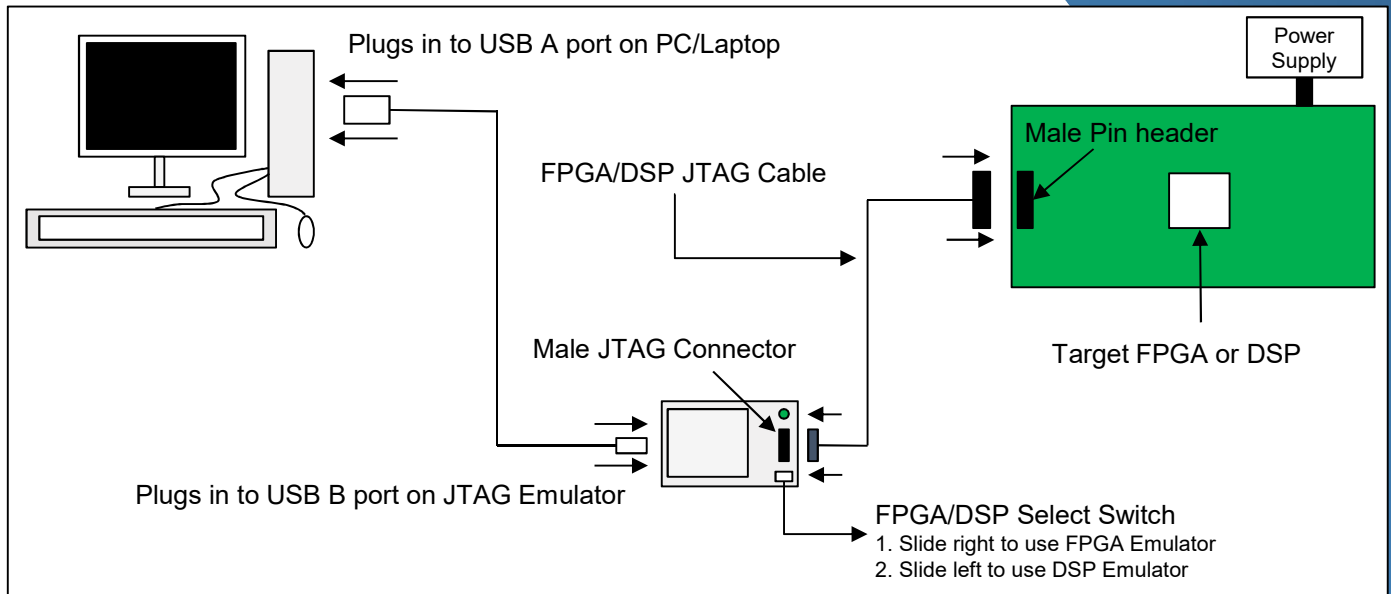
2. DSP board pin assign



Warning !

Do **not** connect or disconnect the emulator cable while the target system is powered up.

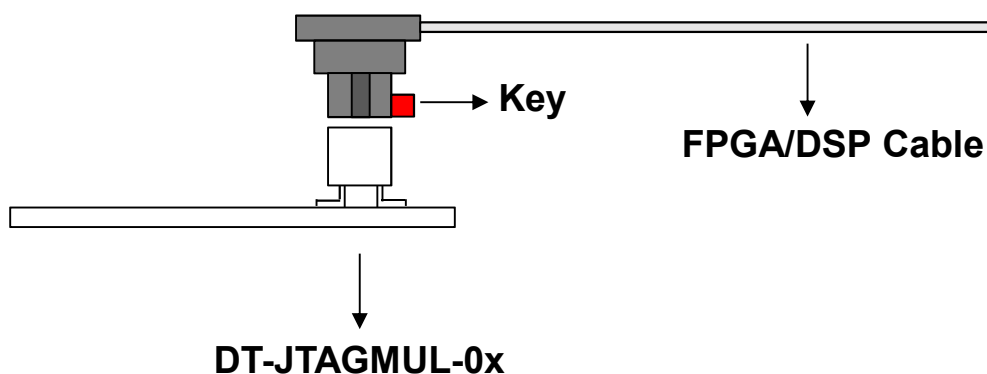
How to use



※ Caution

Before connecting Target board and Emulator using FPGA/DSP cable, check each pin and socket to connect right. Because the connector of the JTAG emulator has a groove, when inserting the cable into the socket, you must check the direction carefully so that the socket key can fit into the groove of the connector.

Please refer to the image below



Dimension

